

Claims

We claim:

1 1. An electronic structure, comprising:
2 a substrate;
3 a first circuit line including a first conductive pad and
4 having a first thickness, wherein the first circuit line is
5 coupled to the substrate; and
6 a second circuit line including a second conductive pad and
7 having a second thickness that is unequal to the first thickness,
8 wherein the second circuit line is coupled to the substrate, and
9 wherein the second circuit line is electrically coupled to the
10 first circuit line.

11 2. The electronic structure of claim 1, wherein the first circuit
12 line is in mechanical contact with the second circuit line.

1 3. The electronic structure of claim 1, wherein said electrical
2 coupling between the first circuit line and the second circuit
3 line includes a third circuit line coupled to the substrate,
4 wherein the third circuit line has a third thickness that is
5 unequal to both the first thickness and the second thickness,
wherein a portion of the third circuit line is electrically
coupled to a portion of the first circuit line, and wherein a
portion of the third circuit line is electrically coupled to a
portion of the second circuit line.

4 4. The electronic structure of claim 1, wherein an end of the
first circuit line includes the first conductive pad, and wherein
an end of the second circuit line includes the second conductive
pad.

5 5. The electronic structure of claim 1, further comprising a
protective coating that covers a portion of a circuit line,
3 wherein the circuit line includes the first circuit line and the
4 second circuit line.

1 6. The electronic structure of claim 1, wherein the first circuit
2 line is mechanically coupled to a first surface of the substrate,
3 and wherein the second circuit line is mechanically coupled to a
4 second surface of the substrate.

5 Sub 1
6 7. The electronic structure of claim 6, wherein said electrical
7 coupling of the second circuit line to the first circuit line
8 includes a plated through hole (PTH), wherein a portion of the
9 first circuit line is coupled to a first end of the PTH, and
10 wherein a portion of the second circuit line is coupled to a
11 second end of the PTH.

12 8. The electronic structure of claim 1, wherein the first circuit
13 line is mechanically coupled to a surface of the substrate, and
14 wherein the second circuit line is mechanically coupled to the
15 surface.

1 9. The electronic structure of claim 1, further comprising:
2 a first solder ball coupled to the first conductive pad;
3 an electronic assembly coupled to the first solder ball;
4 a second solder ball coupled to the second conductive pad;
5 and
6 an electronic carrier coupled to the second solder ball.

1 10. The electronic structure of claim 9, wherein a diameter of
2 the second solder ball is unequal to a diameter of the first
3 solder ball.

1 11. The electronic structure of claim 1, wherein the first
2 conductive pad includes a metallic layer, and further comprising:
3 a first metallic coating over the metallic layer; and
4 a second metallic coating over the first metallic coating,
5 wherein the first metallic coating inhibits diffusion of a metal
6 from the second metallic coating into the metallic layer.

1 12. The electronic structure of claim 11, further comprising:
2 a wirebond interconnect coupled to the first conductive pad
3 at the second metallic coating;
4 an electronic assembly coupled to the wirebond interconnect;
5 a solder ball coupled to the second conductive pad; and
an electronic carrier coupled to the solder ball.

1 13. The electronic structure of claim 12, wherein the metallic
2 layer includes copper, wherein the first metallic coating
3 includes nickel, wherein the metal of the second metallic coating
4 is selected from the group consisting of gold and palladium, and
5 wherein the wirebond interconnect includes a gold wire.

14. An method for forming an electronic structure, comprising:

- providing a substrate;
- forming a first circuit line that includes a first conductive pad and has a first thickness;
- coupling the first circuit line to the substrate;
- forming a second circuit line that includes a second conductive pad and has a second thickness that is unequal to the first thickness;
- coupling the second circuit line to the substrate; and
- electrically coupling the second circuit line to the first circuit line.

15. The method of claim 14, further comprising mechanically coupling the second circuit line to the first circuit line.

1 16. The method of claim 14, wherein the electrically coupling
2 step includes:

3 forming a third circuit line, coupled to the substrate,
4 having a third thickness that is unequal to both the first
5 thickness and the second thickness;

6 electrically coupling a portion of the third circuit line
7 with a portion of the first circuit line; and

8 electrically coupling a portion of the third circuit line
9 with a portion of the second circuit line.

10 17. The method of claim 14, wherein the step of forming a first
11 circuit line comprises forming the first conductive pad at an end
12 of the first circuit line, and wherein the step of forming a
13 second circuit line comprises forming the second conductive pad
14 at an end of the second circuit line.

15 18. The method of claim 14, further comprising:

16 2 selecting a circuit line that includes the first circuit
17 line and the second circuit line;

18 4 forming a protective coating over a portion of the circuit
19 line.

1 19. The method of claim 1, wherein the step of coupling the first
2 circuit line to the substrate includes mechanically coupling the
3 first circuit line to a first surface of the substrate, and
4 wherein step of coupling the second circuit line to the substrate
5 includes mechanically coupling the second circuit line to a
6 second surface of the substrate.

1 20. The method of claim 19, wherein the electrically coupling
2 step comprises:

3 forming a plated through hole (PTH) through a thickness of
the substrate;

4 coupling a portion of the first circuit line to a first end
of the PTH; and

5 coupling a portion of the second circuit line to a second
end of the PTH.

6 21. The method of claim 14, wherein the step of coupling the
first circuit line to the substrate includes mechanically
coupling the first circuit line to a surface of the substrate,
and wherein the step of coupling the second circuit line to the
substrate includes mechanically coupling the second circuit line
to the surface.

1 28. The method of claim 14, further comprising:

2 coupling a first solder ball to the first conductive pad;

3 coupling an electronic assembly to the first solder ball;

4 coupling a second solder ball to the second conductive pad;

5 and

6 coupling an electronic carrier to the second solder ball.

1 23. The method of claim 22, wherein the step of coupling a second
2 solder ball to the second conductive pad includes coupling the
3 second solder ball, having a diameter that is unequal to a
4 diameter of the first solder ball, to the second conductive pad.

5 24. The method of claim 14, wherein the first conductive pad
6 includes a metallic layer, further comprising:

7 forming a first metallic coating over the metallic layer;

and

forming a second metallic coating over the first metallic
coating, said first metallic coating inhibiting diffusion of a
metal from the second metallic coating into the metallic layer.

1 25 The method of claim 24, further comprising:

2 forming a wirebond interconnect to the first conductive pad

3 at the second metallic coating;

4 coupling an electronic assembly to the wirebond

5 interconnect;

6 forming a solder ball coupled to the second conductive pad;

7 and

8 coupling an electronic carrier to the solder ball.

1 26. The method of claim 24, wherein the metallic layer includes
2 copper, wherein the first metallic coating includes nickel,
3 wherein the metal of the second metallic coating includes gold or
4 palladium, and wherein the wirebond interconnect includes gold.